AVS-1030 Wireless Communications Processor

The Next Level of Software Defined Radio

The AVS-1030 leads the way into the next generation of Software Defined Radios by combining the flexibility of FPGAs and the power of multi-core processors on a single platform.
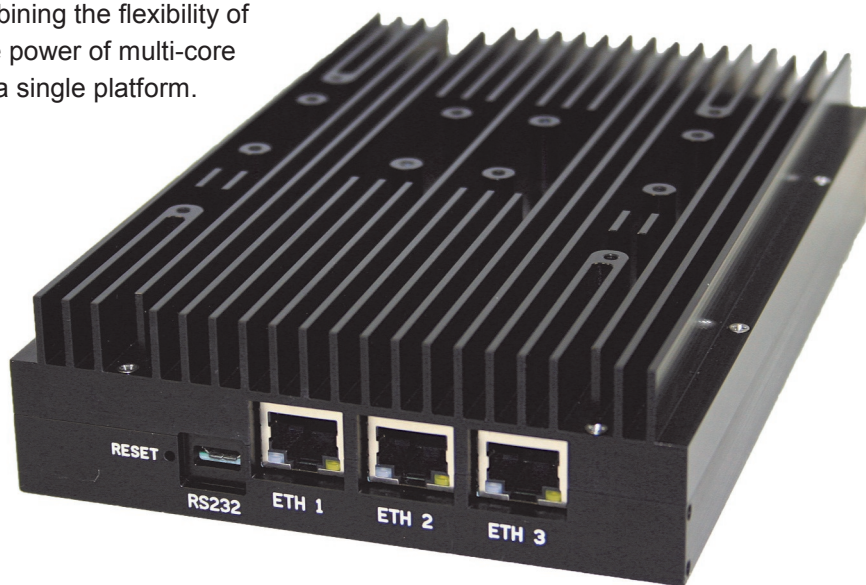
Rugged Stand Alone Chassis

KEY FEATURES:

- Two picoChip^{TM} PC203-10 273 core Base Station PHY Layer Processor
- Optional dual PC203-10 expansion card provides for 4 total PC203-10 processors
- Each PC203-10 has 128 MB of memory
- Xilinx SX50T FPGA; optional SX95T; FPGA has 128 MB DDR2 memory
- 833 MHz powerQuicc processor running the Linux operating system with 512MB
- Half length 8 lane PCI Express. Each lane capable of full duplex 2.5 GT/sec
- Three RJ-45 Gigabit Ethernet ports
- Dual RS232 interface
- Optional rugged stand alone chassis (shown above)
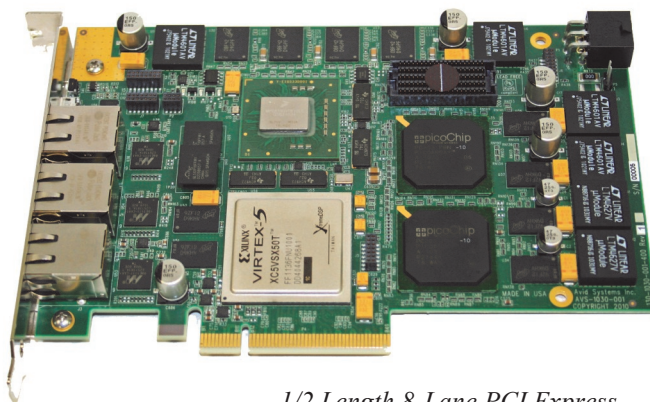
See other side.

avid SYSTEMS, INC

AVS-1030 Wireless Communications Processor

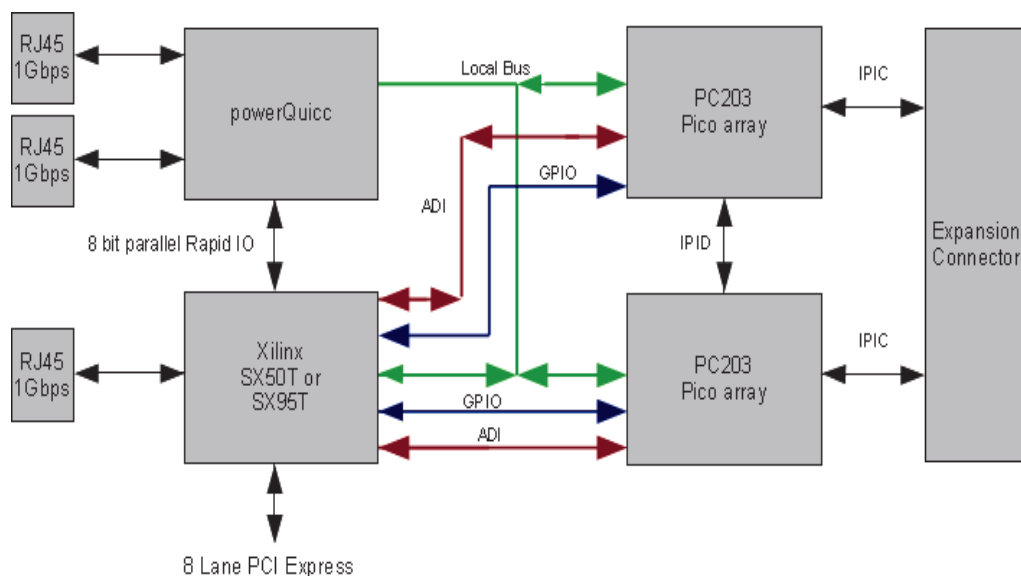## *The Next Level of Software Defined Radio*

PC203 Capabilities:

- 31 GMAC/S 160 GIP/S
- Quad FFT/IFT Engine
- Dual Correlation Blocks
- Integrated Cryptographic Engine AES/DES/3DES
- FEC Accelerator Turbo, Reed Solomon, Viterbi
- Proven 4G and wireless IP available

1/2 Length 8-Lane PCI Express

Architecture

See other side.

avid SYSTEMS, INC